



LET'S INNOVATE TOGETHER

**YOUR TRUSTED
PARTNER FOR
COMPLEX FPGA
& EMBEDDED DESIGNS**



From defense-grade
VPX systems to
AI-driven IP — scalable,
high-performance design
solutions, tailored to your
toughest challenges

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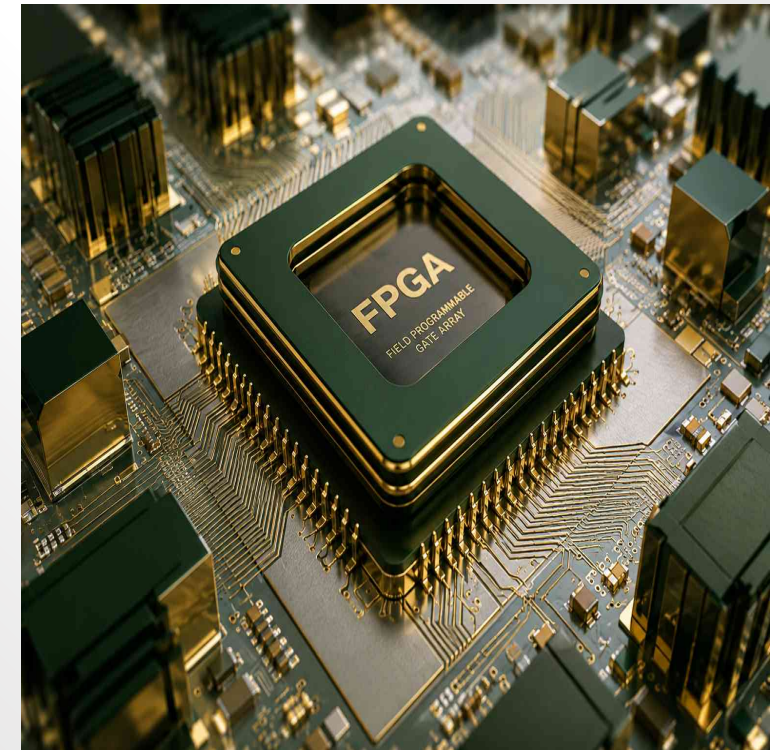
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**End-to-End FPGA Design |
IP Development | System Integration**
**ACCELERATING IDEAS
INTO INTELLIGENT
SILICON**

FPGA Design Capabilities

FPGA Services:

- RTL Design (Verilog, SystemVerilog & VHDL)
- Custom FPGA IP Development
- High-Speed Interface Design
- Verification, Simulation & Hardware Validation
- Timing, Power & Resource Optimization
- FPGA Prototyping
- Design Migration

Hardware Expertise:

- AMD (Xilinx): Zynq, UltraScale, 7 Series.
- Intel (Altera): Cyclone, Arria, Stratix.
- Lattice: CertusPro-NX, CrossLink-NX, MachXO, ispMACH.
- Custom FPGA Boards.

Core Technologies:

- PCIe Gen3/Gen4
- DDR3/DDR4
- 1G / 10G Ethernet
- USB 3.0
- SPI, I2C, UART
- AXI4 Interfaces
- RF Signal Processing
- DDC, DUC, FFT
- AI Acceleration
- Embedded Linux Integration

FPGA Projects

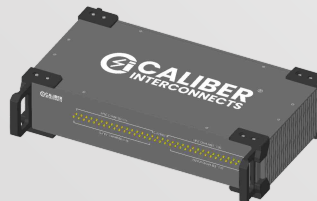
3U VPX RF Data Acquisition Card:



Highlights:

- Dual Kintex UltraScale KU115 FPGAs
- 75 MHz–6 GHz RF Front End
- 200 MHz Instantaneous Bandwidth
- PCIe Gen3 x8, 10G Ethernet & Optical Interfaces
- Up to 64-Channel DDC & FFT Processing
- Rugged Conduction-Cooled VPX Design

32-Channel RFSoc Data Acquisition & Waveform Generator:



Highlights:

- Dual AMD RFSoc ZU49DR
- 32 Rx & 32 Tx Channels
- Up to 9.85 GSPS Waveform Generation
- Real-Time DDC/DUC Processing
- High-Speed Recording (Up to 8 TB)
- 10G Optical Interface (Upgradeable to 25G)

FPGA Projects

AI Acceleration on FPGA



Highlights:

- Security Surveillance
- Automatic Number Plate Recognition (ANPR)
- Face Recognition
- FPGA-Accelerated AI Inference
- Upto 4x1080p RTSP Video Streams
- End-to-End Video Analytics Pipeline

FPGA-Based Silicon Test Kit

Highlights:

- Automated FPGA-Based Test Framework
- Power Sequencing & Device Control
- Protocol Verification
- Real-Time Data Acquisition
- High-Speed Test Automation
- Scalable DUT Validation Platform